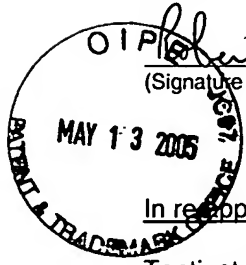


JCOG Rec'd PCT/PTO 13 MAY 2005 CT

I hereby certify that this correspondence is being deposited via first class mail with the United States Postal Service addressed to : Commissioner of Patents and Trademarks, Alexandria, VA 22313, on May 11, 2005. The applicant and/or attorney requests the date of deposit as the filing date. Depositor: Robert Faber



(Signature & date)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of \_\_\_\_\_ :

May 11, 2005

Tonti, et al. \_\_\_\_\_ :

Group Art Unit:

Serial No: PCT/US03/13392 \_\_\_\_\_ :

Examiner:

Filed: 11 April 2003 \_\_\_\_\_ :

International Business Machines Corporation  
2070 Route 52  
Hopewell Junction, NY 12533

**TITLE:** Programmable Semiconductor Device

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the US and non-US patents and publications as listed on Form PTO-1449, attached hereto.

In citing these documents, no representation is made nor intended as to the pertinency or non-pertinency of the art, that better art than that listed is not available, or that other art is not applicable.

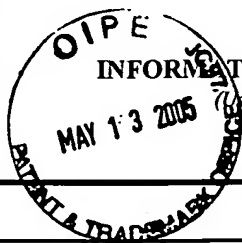
No fee is believed to be due for this submission. If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,  
Tonti, et al.

By \_\_\_\_\_

Joseph P. Abate  
Registration No. 30,238  
Telephone No. 845-894-4633

BUR920020076PCT1



INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

BUR920020076PCT1

Application Number

PCT/US03/13392

Applicant(s)

Tonti, et al.

Filing Date

11 April 2003

Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

		Kothandaraman, et al. "Electrically Programmable Fuse (eFUSE) Using Electromigration in Silicides". IEEE Electron Device Letters, Vol. 23, No. 9, September 2002.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.